

# Shishir Rajbhandari

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1303 Ocala Rd, Apt 213, Tallahassee, FL 32304

**OBJECTIVE:** To obtain the best hands on experience in the field of Electrical Engineering; thus, increasing my problem solving skills and knowledge.

## EDUCATION:

- **Florida State University, Tallahassee, FL**
- **B.S. in Electrical Engineering** **GPA : 3.50**

## RELEVANT COURSES:

- Power System Analysis
- Analog Control System
- Microelectronics
- Analog Communications
- Digital Logic
- Power Electronics
- Electromagnetic Fields I & II
- Solid State Electronics
- Microprocessor –HCS12
- Intro. to C++

## SKILLS:

- **Software:** Matlab w/ Simulink, PLECS, Orcad, CodeWarrior, Autocad, Altera Quartus II, Xilinx, ModelSim, OmniGraffle, Microsoft Visual Studio
- **Programming Languages:** Assembly, VHDL, C++
- **Operating Systems:** Windows, Macintosh
- **Languages:** Fluent in English, Nepalese, Hindi

## EXPERIENCE:

### **CENTER FOR ADVANCED POWER SYSTEM, TALLAHASSEE, FL**

#### **RESEARCH EXPERIENCE FOR UNDERGRADUATES (REU)**

**JAN 2011 – PRESENT**

- Assist graduate students in non-linear study and simulations for development of a solid-state transformer (SST) for residential applications

### **TALLAHASSEE COMMUNITY COLLEGE, TALLAHASSEE, FL**

#### **LEARNING ASSISTANT (CRLA CERTIFIED)**

**MAY 2008 – PRESENT**

- Tutor college students in Mathematics and Physics
- Mentor students in developing independent learning methods as per Myer-Briggs and Socrates systems

### **SHELL GAS CORP., LAKE WORTH, FL**

#### **ASSISTANT MANAGER**

**JAN 2005 – DEC 2007**

- Prepare sales and expense journals
- Inventory maintenance

## PROJECTS:

- Design, Construction, and Testing of a carbon fiber Solar Car for American Solar Challenge 2012 (Senior Design Team Project)
- Design, Control, and Simulation of a Buck Converter (Power Electronics)
- 31-bus interconnected Power System (Power System Analysis)
- Electric Aircraft w/ PV charge system (All Electric Aircraft)
- Single Tone Multi Frequency (Microprocessor)
- Dual Tone Multi Frequency (Digital Logic)

**AFFILIATION:**

- IEEE 2011

**AWARDS:**

- REU scholarship 2011
- President List 2010
- Deans List 2009/2010
- Inductee – Eta Kappa Nu 2010/2011
- Member – National Honor Society 2009
- Member – Engineers Without Borders 2008
- Medallion Scholastic Achievement Scholarship 2005 – 2010

**REFERENCES:**

- Dr. Simon Foo Professor Foo@eng.fsu.edu
- Dr. Hui Li Professor [hli@caps.fsu.edu](mailto:hli@caps.fsu.edu)
- Dr. Rajendra Arora Professor [arora@eng.fsu.edu](mailto:arora@eng.fsu.edu)
- Dr. Leonard J. Tung Professor tung@eng.fsu.edu