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Lecture #11: Transient Analysis (Part II)

EEL 3003
Introduction to Electrical Engineering
Summer Semester, 2013
Instructor: Dr. Michael Frank

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Administrative Announcements

- Handing back Quiz #1&2 papers...
 - Plan to hand back Quiz #3 on Thurs.
- Announcing **MIDTERM EXAM**...
 - In-class next Tuesday, June 25th
 - Covering chapters 2-4
- Outline of today's class session:
 1. Review Quiz #3 solutions
 2. Continue Chapter 5, Transient Analysis
 - Cont. §5.2 – Writing Differential Equations for Circuits Containing Inductors and Capacitors
 - §5.3 – DC Steady-State Solution of Circuits Containing Inductors and Capacitors – Initial and Final Conditions
- Announcing Homework Assignment #4:
 - Read Ch. 5 & practice w. these exercises:
 - 5.7, 5.24, 5.35*, 5.39*, 5.74, 5.76, 5.79, 5.80
 - Quiz date no sooner than Thu., June 27th

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1. Review Quiz #3 solution

- Grade histogram so far (17/68 papers graded):
 - A – XX
 - B – XXXX
 - C – X
 - D – XX
 - F – XXXXXXXX
- The plan is:
 - Hand back all quiz papers by this Thurs. so you can study what you missed for the exam.

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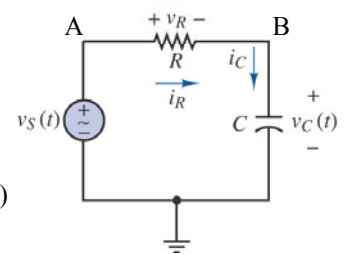


§5.2 – Writing Differential Equations for Circuits Containing Inductors and Capacitors

- On board last time, we worked out this ODE for this circuit using KVL:

$$\frac{di}{dt} + \frac{1}{RC}i - \frac{1}{R} \frac{dv_S}{dt} = 0 \quad (1)$$
- In-class assignment:
 - Start with KCL at node B, and derive a different ODE for this circuit that fits this general form:

$$(a) \frac{dv_C}{dt} + (b)v_C + (c)v_S = 0 \quad (a, b, c \text{ constants}) \quad (2)$$



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Another Example (Ex. 5.1, pp. 218-219)

- Derive diff. eq. for $i_L(t)$,
given these values:
 - $R_1 = 10 \Omega$
 - $R_2 = 5 \Omega$
 - $L = 0.4 \text{ H}$
- Start w. KCL @ node B:
- Expand terms:

$$i_{R_1} - i_L - i_{R_2} = 0 \quad (3)$$

$$\frac{v_S - v_L}{R_1} - i_L - \frac{v_L}{R_2} = 0 \quad (4)$$

(cont.)

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Example 5.1, cont.

- Use diff. eq. of inductor
voltage to elim. v_L fr. eq.:
- Then grouping:


$$\frac{v_S}{R_1} - \frac{L}{R_1} \frac{di_L}{dt} - i_L - \frac{L}{R_2} \frac{di_L}{dt} = 0 \quad (5)$$

$$\left(\frac{L}{R_1} + \frac{L}{R_2} \right) \frac{di_L}{dt} + i_L = \frac{1}{R_1} v_S \quad (6)$$

plug in values


$$(0.12 \text{ s}) \frac{di_L}{dt} + i_L = (0.1 \text{ S}) v_S \quad (7)$$

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General Form for ODEs for First-Order Circuits




- As illustrated by the last couple of examples,
 - Whenever we have a circuit with only one reactive element, it will have an ODE of this general form:

$$a_1 \cdot \frac{dx(t)}{dt} + a_0 \cdot x(t) = b_0 \cdot f(t) \quad (8)$$


- where:
 - $x(t)$ – A time-varying current or voltage in the circuit.
 - $f(t)$ – Some other time-varying quantity in the circuit.
 - a_1, a_2, b_0 – Constants.
- In-class exercise:
 - Find x, f, a_1, a_2, b_0 for both of the last two examples.

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Properties of the General Form




- Dividing last eq. (8) through by a_0 gives:

-

$$\frac{a_1}{a_0} \frac{dx(t)}{dt} + x(t) = \frac{b_0}{a_0} f(t) \quad (9)$$
- Let us define quantities:
 - $\tau = a_1 / a_0$ – The “time constant”
 - $K_S = b_0 / a_0$ – The “DC gain”
- Then eq. (9) becomes:

$$\tau \frac{dx(t)}{dt} + x(t) = K_S f(t) \quad (10)$$
- Exercise: What are τ and K_S of prev. examples?


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Second-Order Circuit Example

(Example on pp. 220-221)



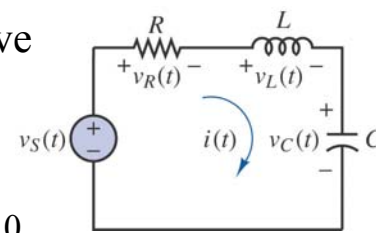
□ Note this circ. has 2 reactive elements (ind. & cap.)

■ KVL equation:


$$v_s(t) - Ri(t) - L \frac{di(t)}{dt} - \frac{1}{C} \int_{\tau=-\infty}^t i(\tau) d\tau = 0 \quad (11)$$

■ Differentiate to get 2nd-order ODE:

$$R \frac{di(t)}{dt} + L \frac{d^2 i(t)}{dt^2} + \frac{1}{C} i(t) = \frac{dv_s(t)}{dt} \quad (12)$$




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Another Form



□ Subst. $i(t) = C dv_C/dt$ into orig. eq. (11) to get:


$$RC \frac{dv_C(t)}{dt} + LC \frac{d^2 v_C(t)}{dt^2} + v_C(t) = v_s(t) \quad (13)$$

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General form of ODEs for Second-Order Circuits




□ Eqs. (12) & (13) can each be expressed in the general form:

$$a_2 \frac{d^2x(t)}{dt^2} + a_1 \frac{dx(t)}{dt} + a_0x(t) = b_0f(t)$$

□ Again we can divide through by a_0 , to get:


$$\frac{a_2}{a_0} \frac{d^2x(t)}{dt^2} + \frac{a_1}{a_0} \frac{dx(t)}{dt} + x(t) = \frac{b_0}{a_0} f(t)$$

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Properties of Second-Order Circuits



□ Now we define:


- ω_n – (“omega sub n”) The *natural frequency*
- ζ – (“zeta”) The *damping ratio*
- K_S – (“K sub S”) The *DC gain*

$$\omega_n = \sqrt{\frac{a_0}{a_2}}, \quad \zeta = \frac{a_1}{2} \sqrt{\frac{1}{a_0a_2}}, \quad K_S = \frac{b_0}{a_0}$$


□ And get:

$$\frac{1}{\omega_n^2} \frac{d^2x(t)}{dt^2} + \frac{2\zeta}{\omega_n} \frac{dx(t)}{dt} + x(t) = K_S f(t)$$

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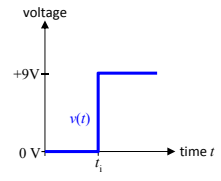


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§5.3 – DC Steady-State Solution of Circuits Containing Inductors & Capacitors – Initial & Final Conditions


- Just before a step impulse at the source,
 - And as $t \rightarrow \infty$ after the step,
- We can assume eventually all voltages and currents in the circuit settle down to stable equil. values.
 - Set all time derivatives equal to 0.



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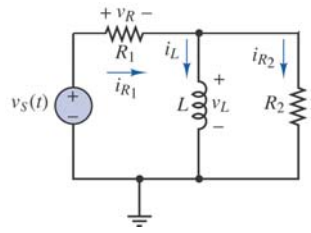
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Steady-State for Example 5.1

- Differential equation was:

$$\underbrace{\left(\frac{L}{R_1} + \frac{L}{R_2} \right)}_{\tau} \frac{di_L}{dt} + \underbrace{i_L}_{K_S} = \frac{1}{R_1} v_S$$
- In steady-state, $di_L/dt \rightarrow 0$ (const. current), so in the limit as $t \rightarrow \infty$,



$$i_L = K_S v_S = \frac{v_S}{R_1} \quad \text{so} \quad v_S = R_1 i_L$$

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Another Method

- Under DC excitation, replace reactances:
 - $L \rightarrow$ closed circuit
 - $C \rightarrow$ open circuit

Thus, clearly, in steady state, $i_L = v_s/R_1$

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Example: Finding the Steady-State Equations of a 2nd-Order Circuit

- Method 1: ODE comes out as:

$$\underbrace{\left(\frac{R_1 C L}{R_1 + R_2}\right)}_{1/\omega_n^2} \frac{d^2 i_L(t)}{dt^2} + \underbrace{\left(\frac{R_1 R_2 C + L}{R_1 + R_2}\right)}_{2\zeta/\omega_n} \frac{di_L(t)}{dt} + \underbrace{i_L(t)}_{K_S} = \underbrace{\frac{1}{R_1 + R_2} v_s(t)}_{K_S}$$
- Set all derivs. = 0 to get:

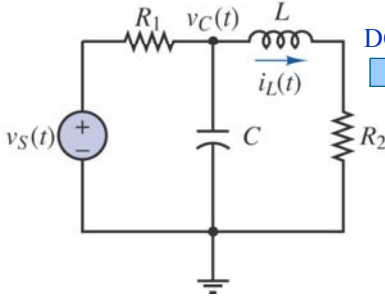
$$i_L(t) = \left(\frac{1}{R_1 + R_2}\right) v_s(t)$$

(fig. 5.8)

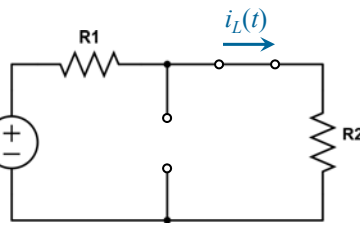
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Or, by simplifying circuit...



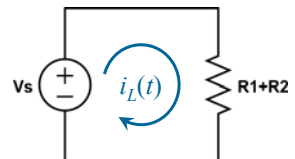
DC equiv.



simplify

$$i_L(t) = \frac{v_S(t)}{R_1 + R_2}$$

write eq.

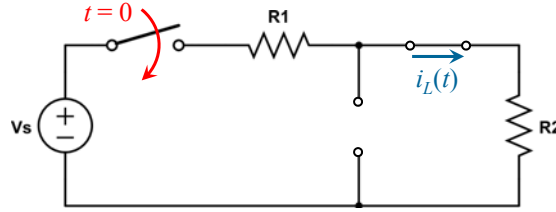


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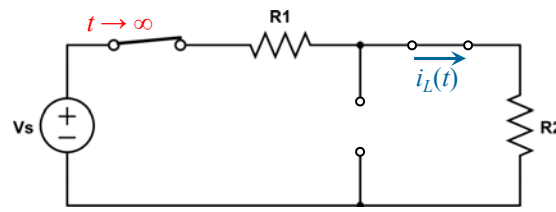
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Initial/Final Conditions



- Initial condition: At time $t = 0$, switch closes:



- Final condition: State as time $t \rightarrow \infty$:



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Continuity Rules

- The main variables for 1st- and 2nd-order transient analysis are usually:
 - v_C – The voltage across a capacitor
 - i_L – The current through an inductor
- *These variables can't change instantaneously!*
- Therefore,

- $v_C(0^+) = v_C(0^-)$
 - $i_L(0^+) = i_L(0^-)$

$$\lim_{\delta t \rightarrow 0} : v_C(0 - \delta t) = v_C(0 + \delta t)$$

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